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RESPONSE UNDER 37 C.F.R. § 1.111
Serial Number: 09/896,523
Filing Date: April 29, 2001
Title: VOLTAGE-LEVEL CONVERTER
Assignee: Intel Corporation

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Dkt: 884.453US1 (INTEL)

REMARKS

Applicant has reviewed and considered the Office action mailed on January 14, 2004 and the references cited therewith.

Claims 29-32 are now pending in the application.

§ 102 Rejection of the Claims

Claims 29-32 were rejected under 35 U.S.C. § 102(e) as being anticipated by Pantelakis *et al.* (U.S. Patent No. 6,275,070). Applicant does not admit that Pantelakis *et al.* is prior art and reserves the right, as provided for under 37 C.F.R. 1.131, to "swear behind" Pantelakis *et al.* Applicant respectfully traverses the rejection of claims 29-32.

Claims 29 and 31 recite, "a split-level output circuit." The Office action, in paragraph 2, indicates that a split-level output circuit is shown at reference number 166 of Pantelakis *et al.* Applicant respectfully disagrees. Pantelakis *et al.* at column 5, line 45 recites "inverter 166." The "inverter 166" is not a "split-level output circuit," so Pantelakis *et al.* fails to teach a "split-level output circuit." Hence, Pantelakis *et al.* fails to teach each of the elements of claims 29 and 31. Thus, the Office action fails to state a *prima facie* case of anticipation with respect to claims 29 and 31. Therefore, applicant requests withdrawal of the rejection and reconsideration and allowance of claims 29 and 31.

Claim 30 is dependent on claim 29. Claim 32 is dependent on claim 31. For reasons analogous to those state above, applicant respectfully submits that the Office action fails to state a *prima facie* case of anticipation with respect to claim 30. Therefore, applicant requests withdrawal of the rejections and reconsideration and allowance of claims 29 and 31.

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CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone applicant's attorney at 612-371-2109 to facilitate prosecution of the application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743

Respectfully submitted,
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By their Representatives,

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Date

April 14, 2004

By

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CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 14th day of April, 2004.

Name

KACIA LEE

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Kacia Lee